



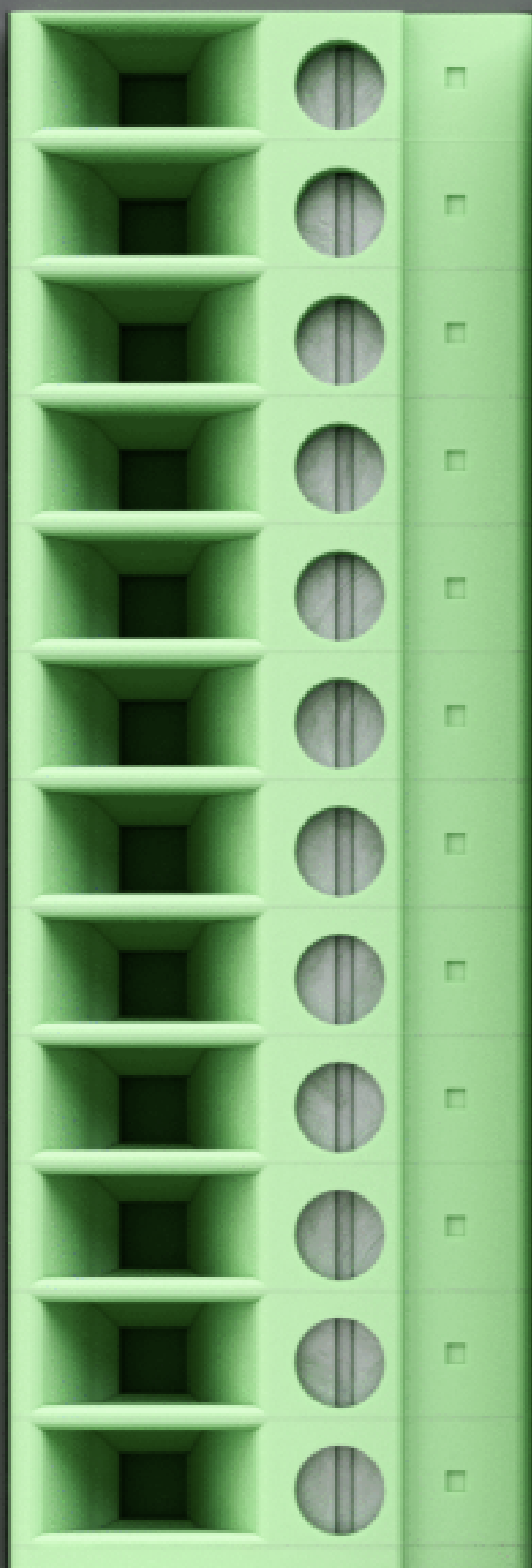
X3

X2

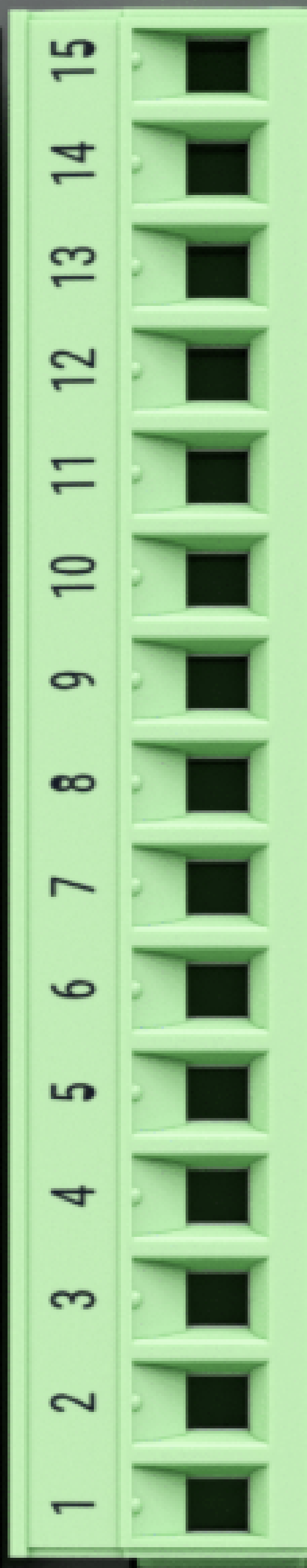
X1



IL1 12
11
IL2 10
9
IL3 8
7
Io 6
5
HSO1 4
3
HSO2 2
1



15
14
13
12
11
10
9
8
7
6
5
4
3
2
1



15
14
13
12
11
10
9
8
7
6
5
4
3
2
1



15
14+ BI2
13+ BO1
12
11 +
10 S1 -
9 -
8 +
7 S2 -
6 -
5 +
4 S3 -
3 -
2 +
1 S4

X5

D+
D-
Shield
Term.



0,5
6,0
0,05
2,0



I> set

x In

Io> set

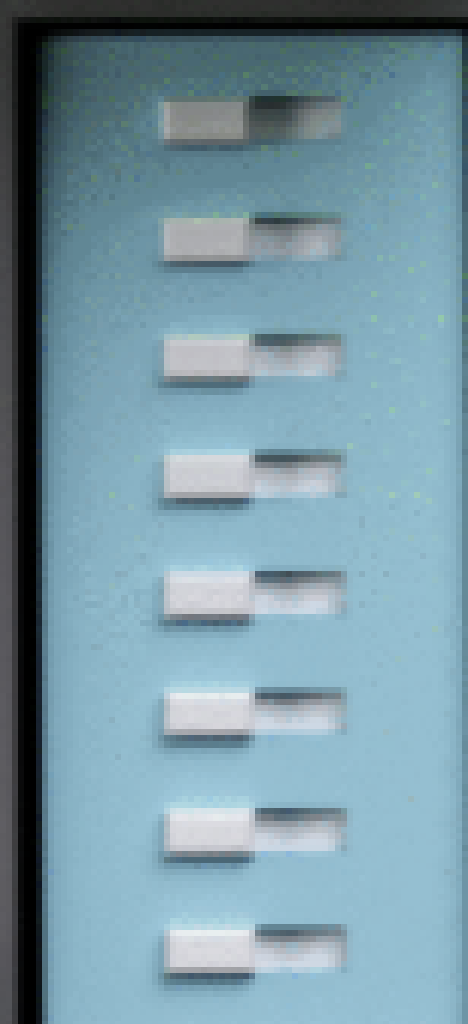


RX
S5



TX
AQD

Baud
Pe/d
Po/e
Stp b
Modbus
address



8
7
6
5
4
3
2
1



T1/T2 Latch
HSO Latch
Block.func.
S5:Fib./AQD
FAST / CBFP
100/150 ms
I> 1A / 5A
Io> 1A / 5A

S1:L>/L>+I>
S2:L>/L>+I>
L>/L>+I>

a/b
Scheme
Selection



8
7
6
5
4
3
2
1

ON SW3 OFF ON SW2 OFF

ON SW1 OFF